

IN THE DRAWINGS

The attached sheets of drawings include changes to FIGS. 8 and 9B. These sheets replace the original sheets. Marked up versions have also been supplied.

REMARKS

This Preliminary Amendment is submitted to fix minor errors.

In FIG. 8, the rows corresponding to K13.7 and K14.7 have been removed because the control characters associated with K13.7 and K14.7 overlap with data
5 vectors. For example, K14.7 used "011100" as part of its primary control vector, but this value is used as a data value in D14 (see FIG. 5).

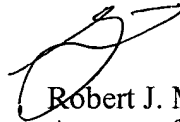
The changes to FIG. 8 necessitated the changes to the specification at pages 5 and 10.

In FIG. 9B, the name "PDE" has been changed to --NDE-- to correspond
10 to terminology used throughout the figures. In this case, the letter "N" is used to indicate that the signal is negated. See, for instance, page 22, lines 24-27 and page 11, line 26 to page 12, line 2.

Additionally, an error of a grammatical nature on page 22 was corrected.

The Examiner's attention to this matter is appreciated.

Respectfully submitted,



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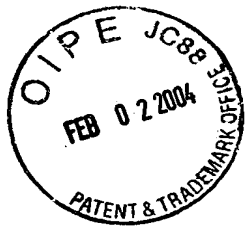


FIG. 7

NAME	ABCDE FGH K	PRIMARY (6B) DR abcdei fghi DB	ALTERNATE DR abcdei fghi DB
K3.0	11000 000 1	+ 110000 1010 -	- 001111 0101 +
K3.1	11000 100 1	+ 110000 0110 -	- 001111 1001 +
K3.2	11000 010 1	+ 110000 1011 0	- 001111 0100 0
K3.3	11000 110 1	+ 110000 1100 -	- 001111 0011 +
K3.4	11000 001 1	+ 110000 1101 0	- 001111 0010 0
K3.5	11000 101 1	+ 110000 0101 -	- 001111 1010 +
K3.6	11000 011 1	+ 110000 1001 -	- 001111 0110 +
K3.7	11000 111 1	+ 110000 1110 0	- 001111 0001 0
K23.7	11101 111 1	- 111010 1000 0	+ 000101 0111 0
K27.7	11011 111 1	- 110110 1000 0	+ 001001 0111 0
K29.7	10111 111 1	- 101110 1000 0	+ 010001 0111 0
K30.7	01111 111 1	- 011110 1000 0	+ 100001 0111 0

FIG. 8

NAME	ABCDE FGH K	PRIMARY (6B) DR abcdei fghi DB	ALTERNATE DR abcdei fghi DB
K11.7	11010 111 1	+ 110100 1000 -	- 001011 0111 +
K13.7	10110 111 1	+ 101100 1000 -	- 010011 0111 +
K14.7	01110 111 1	+ 011100 1000 -	- 100011 0111 +
K19.7	11001 111 1	+ 110010 1000 -	- 001101 0111 +
K21.7	10101 111 1	+ 101010 1000 -	- 010101 0111 +
K22.7	01101 111 1	+ 011010 1000 -	- 100101 0111 +
K25.7	10011 111 1	+ 100110 1000 -	- 011001 0111 +
K26.7	01011 111 1	+ 010110 1000 -	- 101001 0111 +
K28.7	00111 111 1	+ 001110 1000 -	+ 110001 0111 +

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FIG. 9B

The diagram illustrates a complex logic circuit, likely a 6-bit adder, featuring various logic gates and AOI blocks. Key components and connections include:

- Inputs:** PE, NC, NB, PA, PB, PC, PD, PE, PDFS6, NPDFS6, and various carry inputs (e.g., NCnE, NCnD, NCnE, NCnD, NCnE, NCnD).
- Logic Gates:** AND gates (I398, I395, I396, I394, I397, I393, I392, I399, I404, I401, I402, I403, I409, I410, I501, I507, I500, I508, I503, I504, I511, I421, I440, I430, I423, I475, I467), OR gates (I400, I406), XOR gates (I464, I405), and XNOR gates (I474, I473).
- AOI Blocks:** AOI blocks (I429, I474, I469) are used for sum and carry generation.
- Carry Propagation:** The circuit includes logic for carry propagation, such as the carry-in (NCnE) and carry-out (NCnD) signals.
- Sum and Carry Generation:** The circuit generates the sum (PCMPLS6) and carry (PDRS4ueDFS6) signals.
- Handwritten Note:** A note "changed to NDE" is present near gate I506, indicating a modification to the original design.